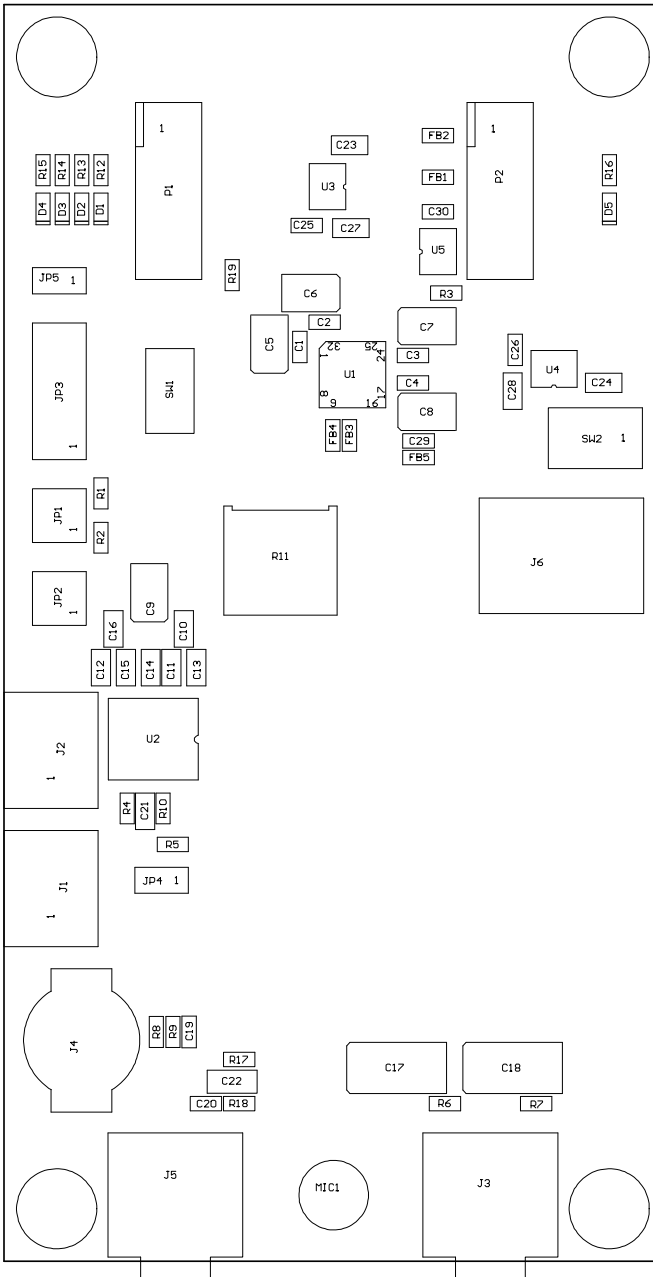




Texas Instruments

DESIGNER:
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TITLE:

CODEC_EVM



ENGINEER:
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PART NO.: CODEC_EVM

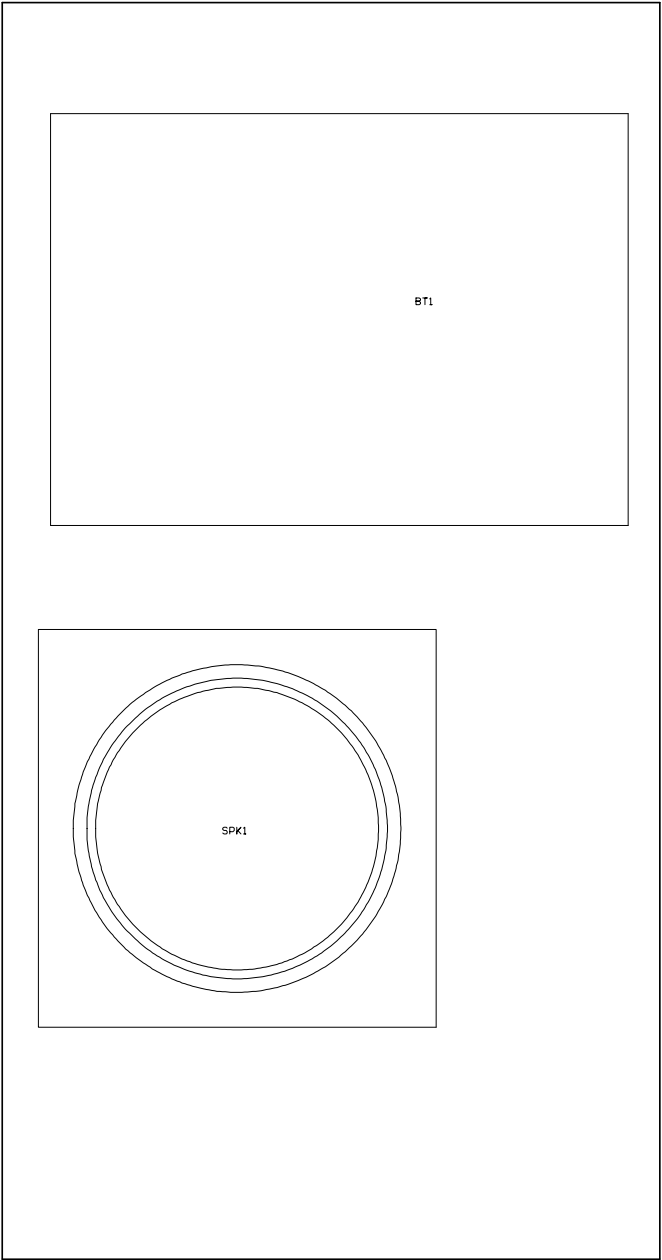
REV: B

DATE: 20-Jan-2009

FILE NAME: CODEC_EVM.PCB

LAYER: Mechanical1

GERBER:



Texas Instruments

DESIGNER:
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TITLE:

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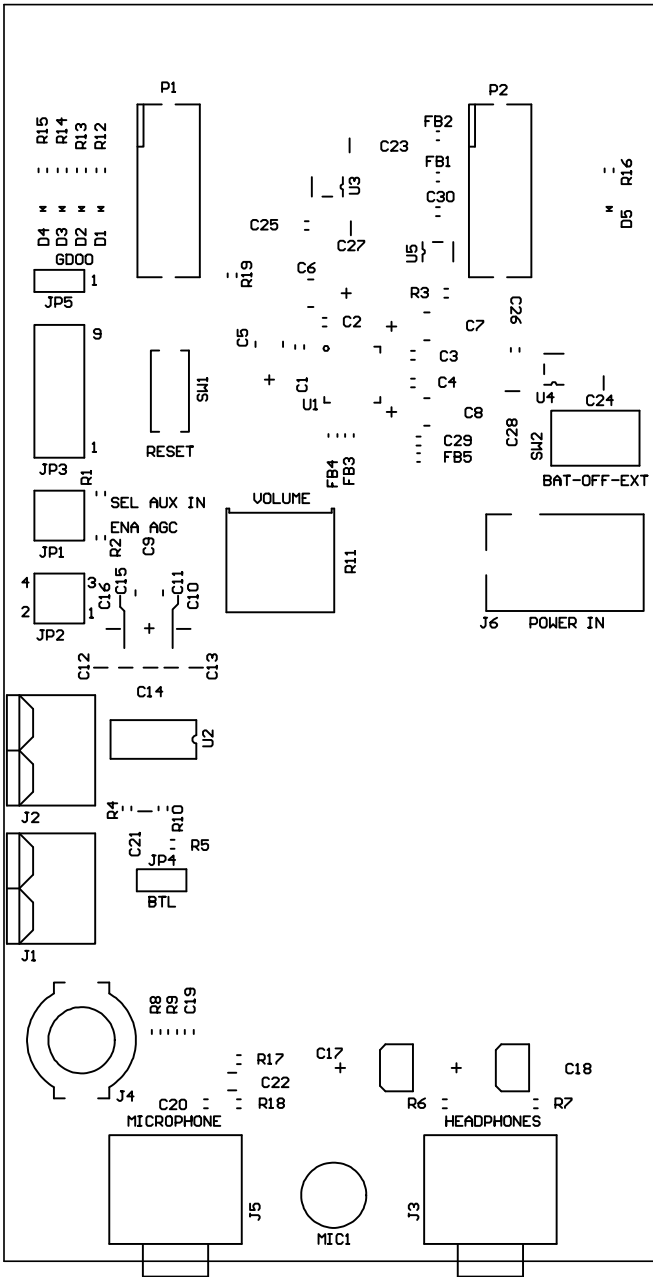
REV: B

DATE: 20-Jan-2009

FILE NAME: CODEC EVM.PCB

LAYER: Mechanical5

GERBER:



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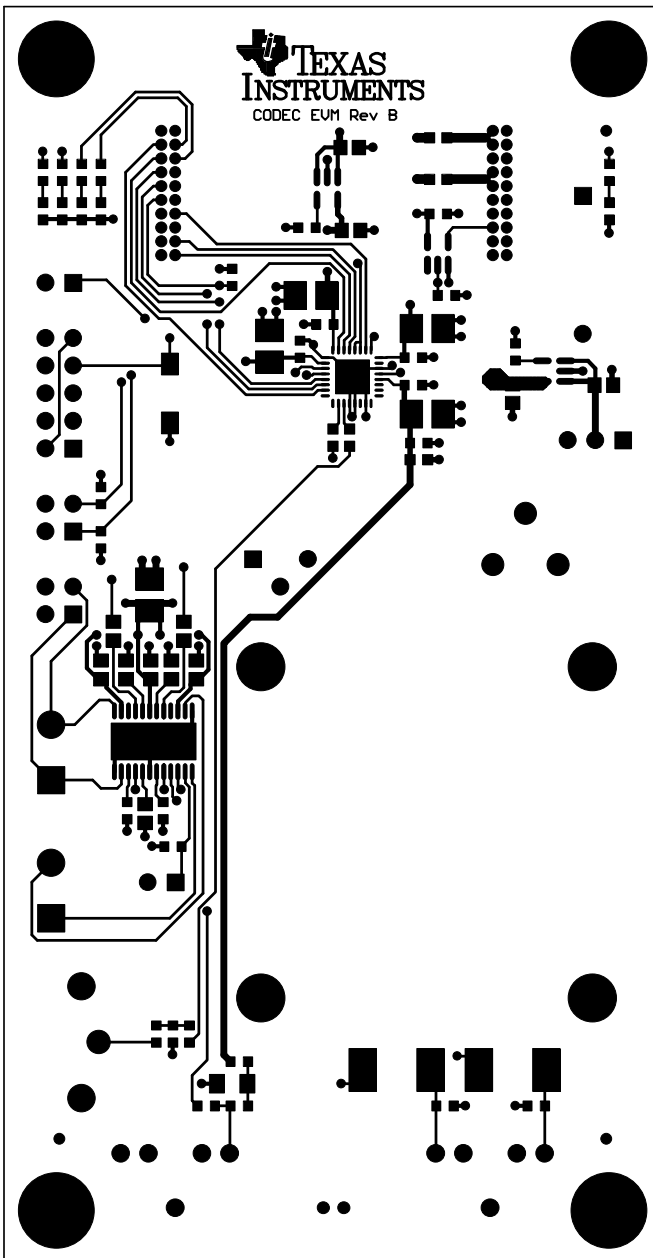
REV: B

DATE: 20-Jan-2009

FILE NAME: CODEC_EVM.PCB

LAYER: Top Overlay

GERBER: .GTO



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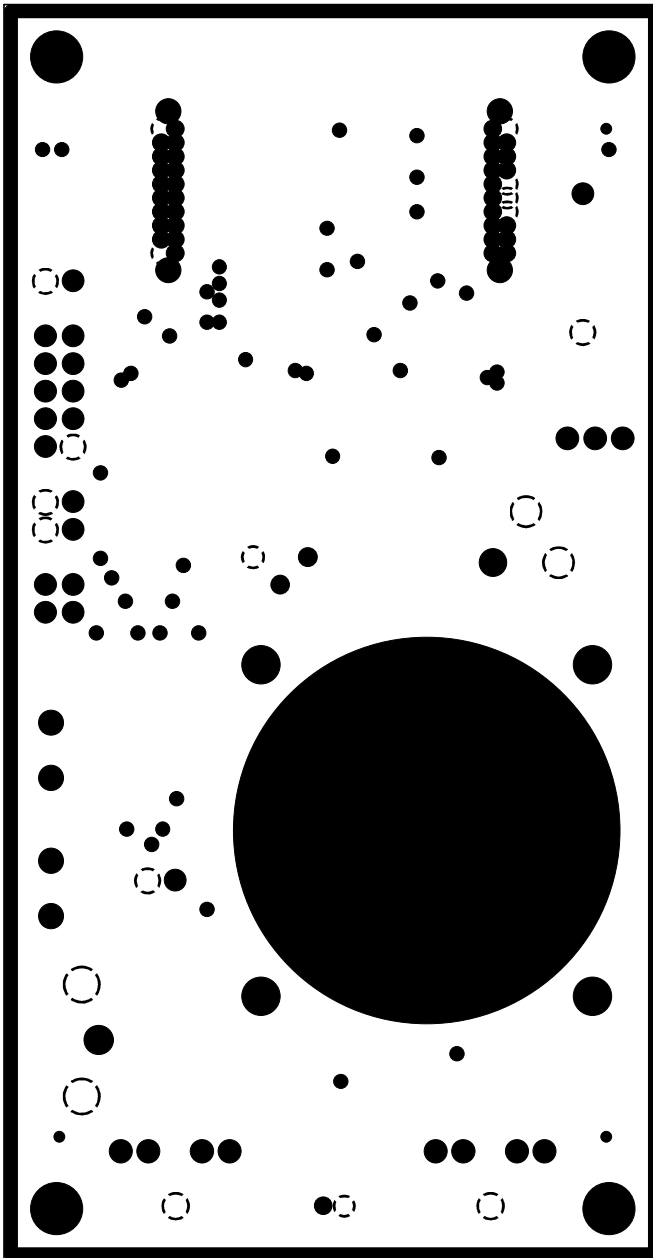
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DATE: 20-Jan-2009

FILE NAME: CODEC EVM.PCB

LAYER: TopLayer

GERBER: .GTL



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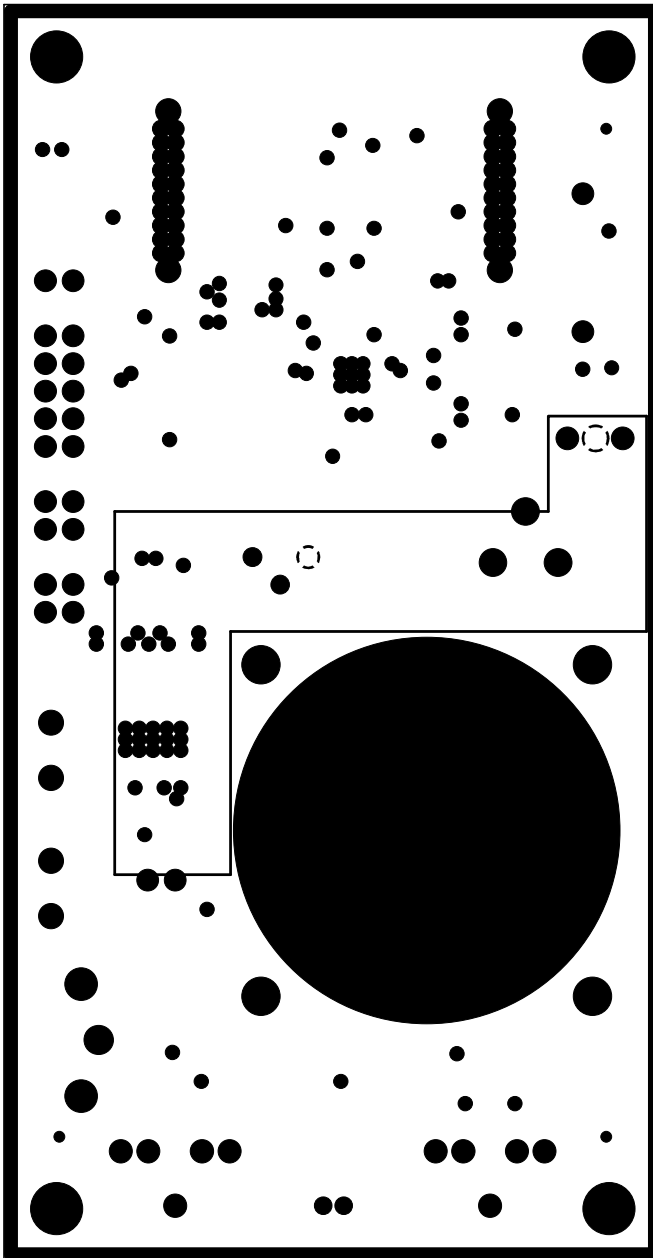
REV: B

DATE: 20-Jan-2009

FILE NAME: CODEC EVM.PCB

LAYER: InternalPlane1

GERBER: .GP1



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TITLE:
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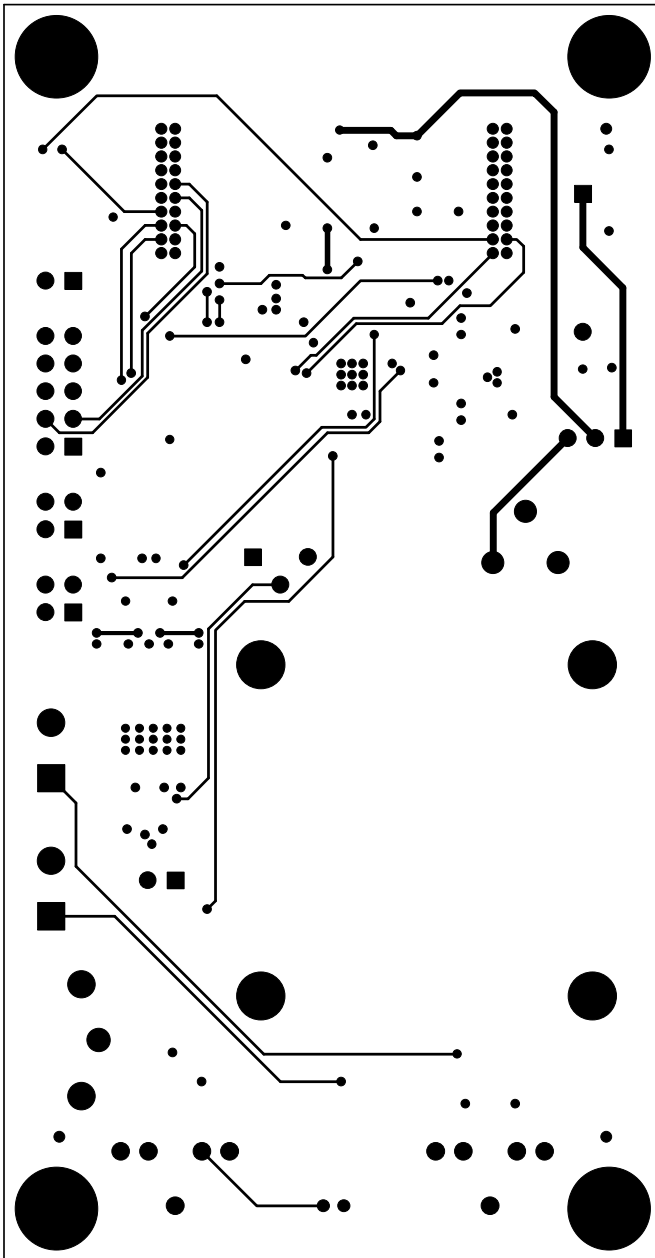
REV: **B**

DATE: **20-Jan-2009**

FILE NAME: **CODEC EVM.PCB**

LAYER: **InternalPlane2**

GERBER: **.GP2**



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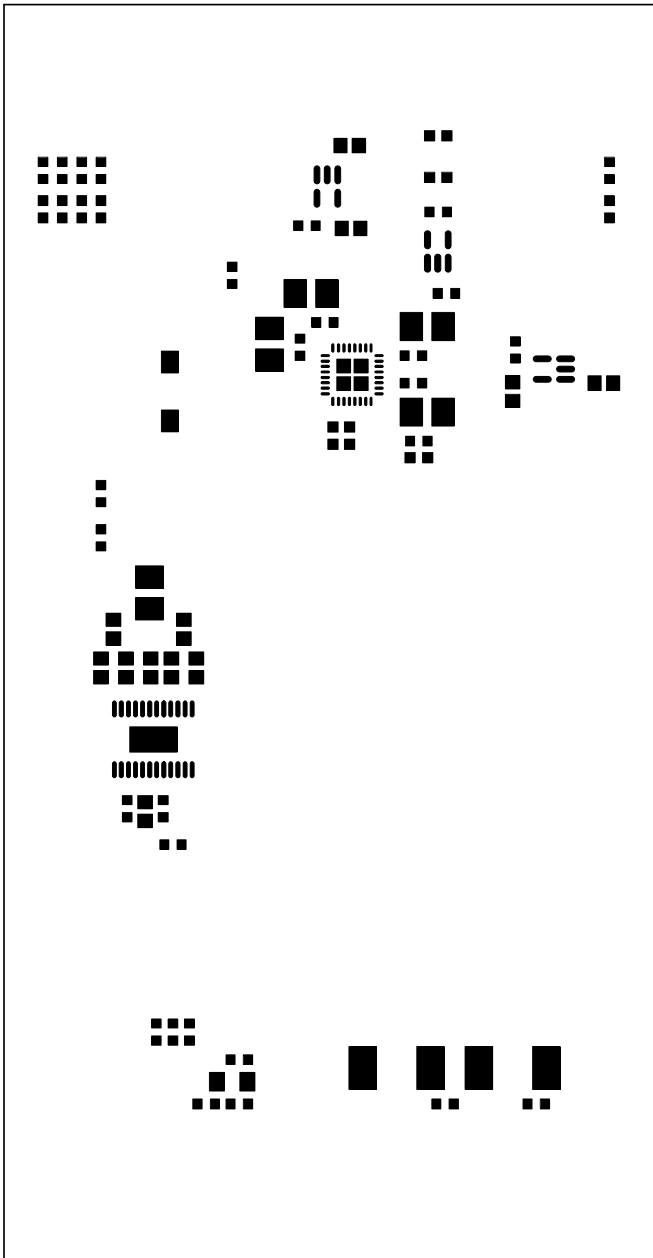
REV: **B**

DATE: **20-Jan-2009**

FILE NAME: **CODEC EVM.PCB**

LAYER: **BottomLayer**

GERBER: **.GBL**



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ENGINEER:
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PART NO.: **CODEC_EVM**

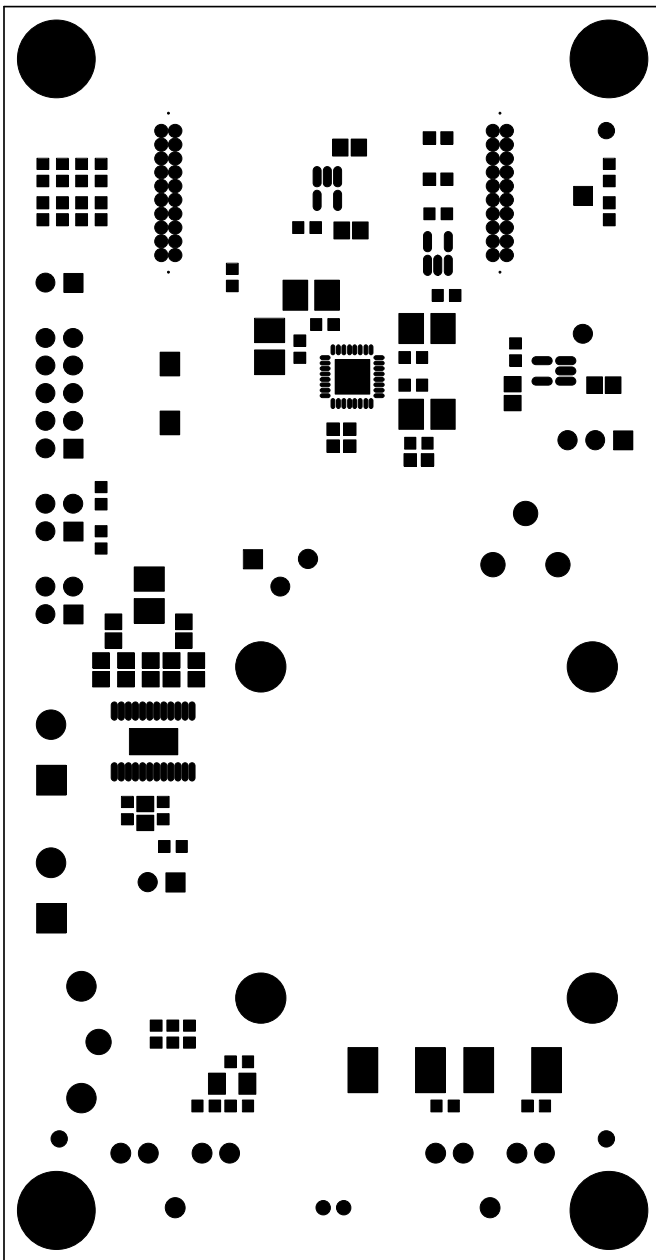
REV: **B**

DATE: **20-Jan-2009**

FILE NAME: **CODEC EVM.PCB**

LAYER: **Top Paste**

GERBER: **.GTP**



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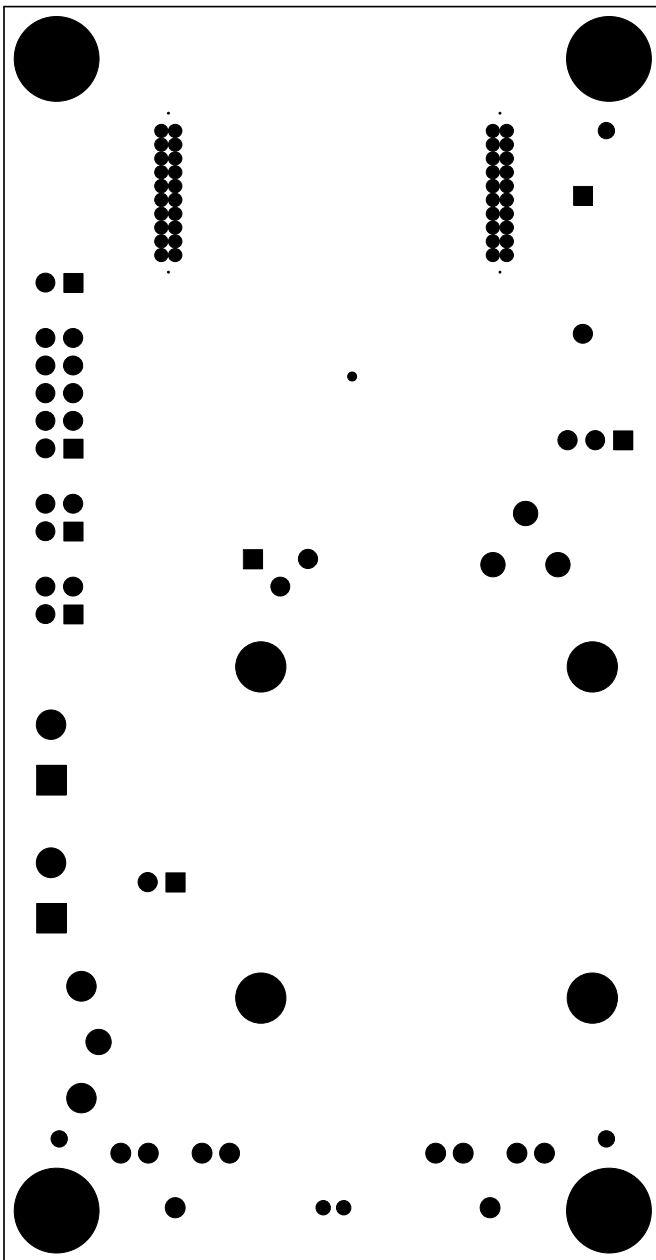
REV: **B**

DATE: **20-Jan-2009**

FILE NAME: **CODEC EVM.PCB**

LAYER: **Top Solder Mask**

GERBER: **.GTS**



Texas Instruments

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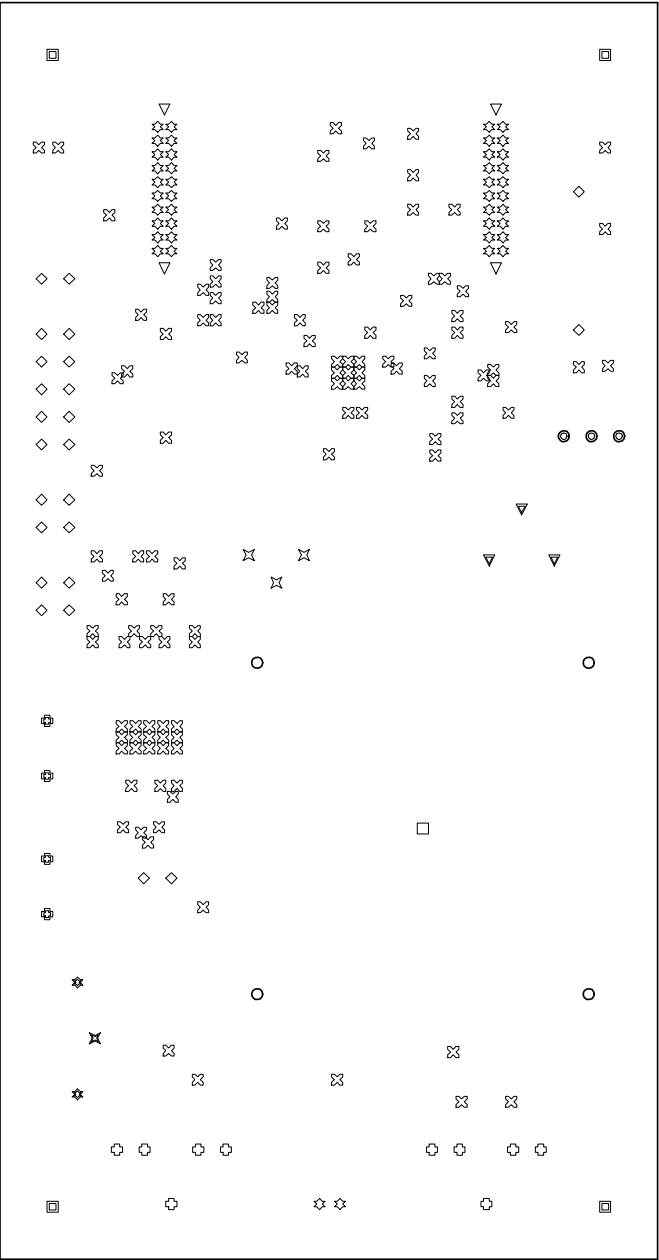
REV: B

DATE: 20-Jan-2009

FILE NAME: CODEC EVM.PCB

LAYER: Bottom Solder Mask

GERBER: .GBS



x	118	13.5mil	0.3429mm	PTH
*	42	25mil	0.635mm	PTH
x	3	29mil	0.7366mm	PTH
d	24	40mil	1.016mm	PTH
o	3	43mil	1.0922mm	PTH
o	10	45mil	1.143mm	PTH
+	4	52mil	1.3208mm	PTH
v	4	52mil	1.3208mm	NPTH
v	3	61mil	1.5494mm	PTH
x	1	67mil	1.7018mm	PTH
*	2	79mil	2.0066mm	PTH
o	4	100mil	2.54mm	PTH
o	4	150mil	3.81mm	PTH
o	1	250mil	6.35mm	NPTH
	223	Total		

Drill Drawing.

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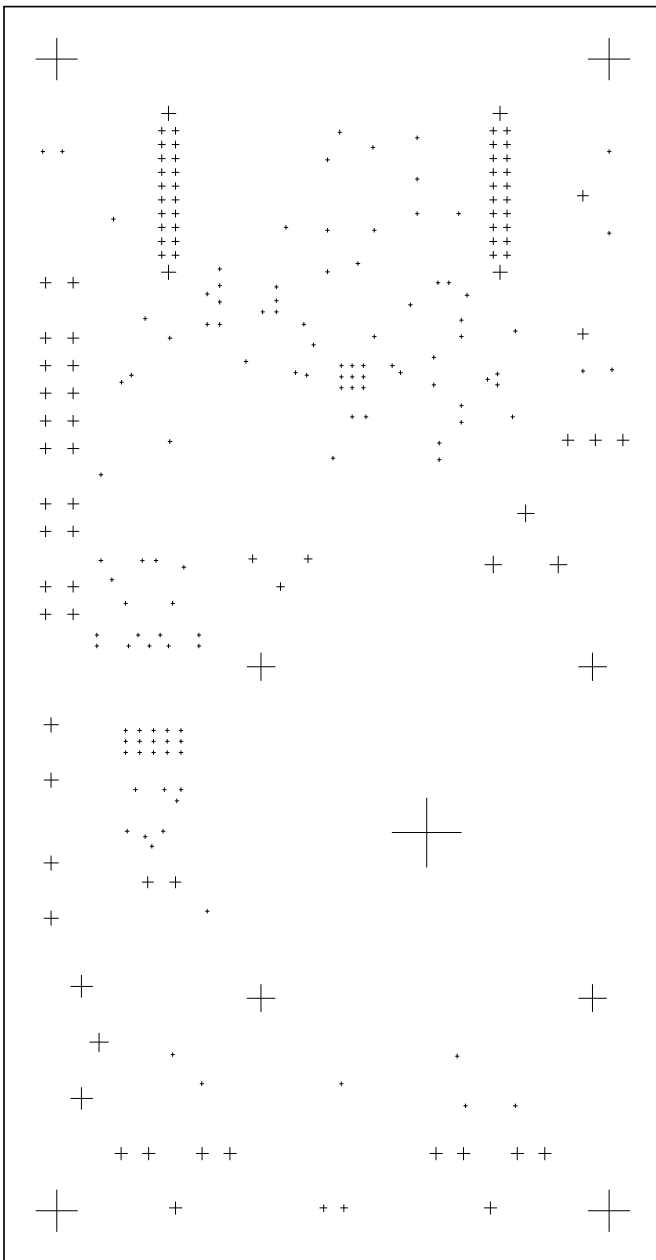
REV: B

DATE: 20-Jan-2009

FILE NAME: CODEC EVM.PCB

LAYER: DrillDrawing

GERBER:



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REV: B

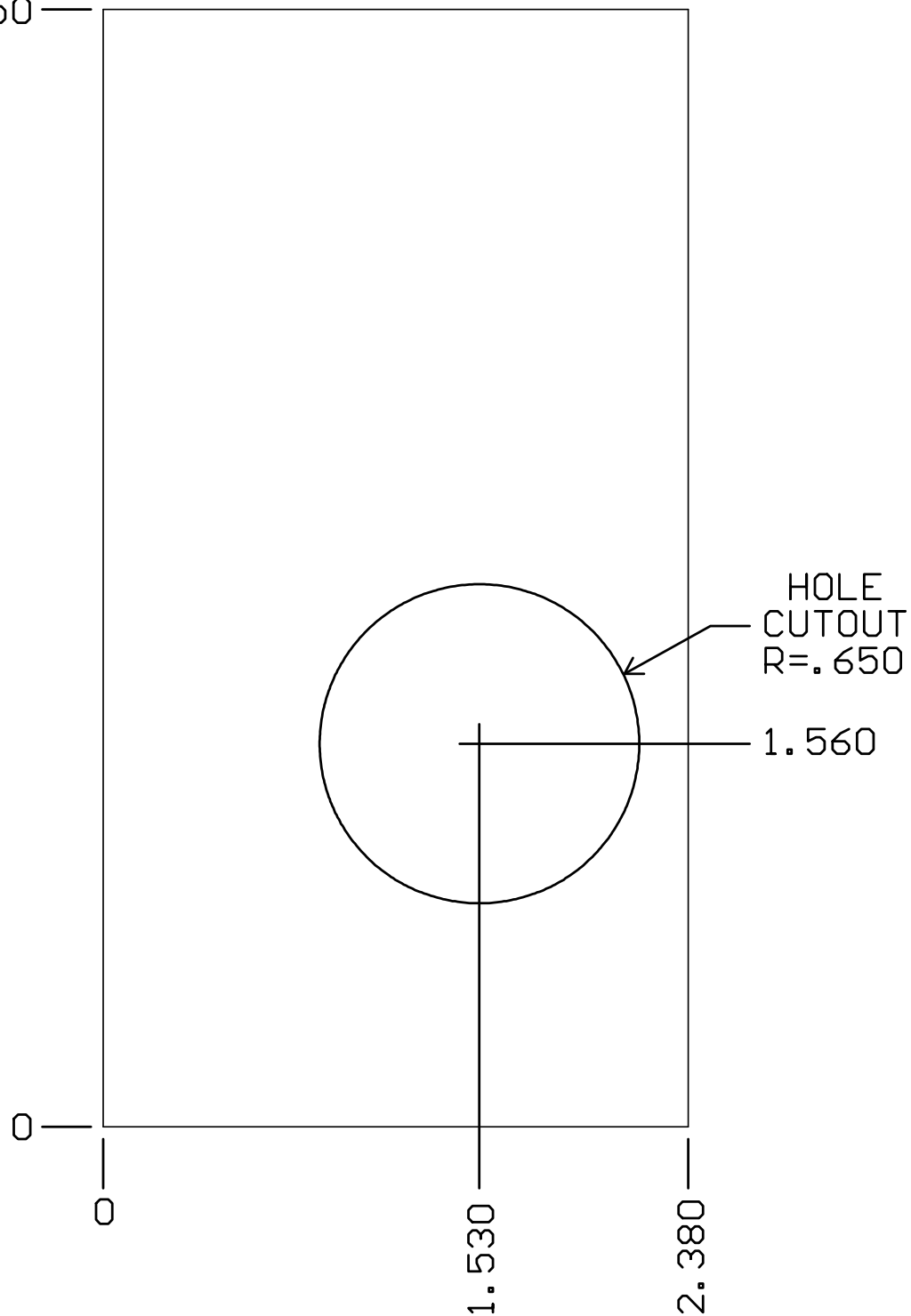
DATE: 20-Jan-2009

FILE NAME: CODEC EVM.PCB

LAYER: DrillGuide

GERBER:

4.550



Texas Instruments

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PART NO.: CODEC_EVM

REV: B

DATE: 20-Jan-2009

FILE NAME: CODEC EVM.PCB

LAYER: Mechanical3

GERBER: